IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: TRAN-P059

Inventor(s): Andrew Read, Sameer Halapete, Keith Klayman

Application No.: 09/694,433 Group Art Unit: 2185

Filed: 10/23/00 Examiner: CAO, Chun

Title: SAVING POWER WHEN IN OR ON TRANSITIONING TO A STATIC MODE OF A PROCESSOR (AS AMENDED)

Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(c)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(c) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

Pat. No.	Pat. Title	Grant Date
6,704,880	REDUCING SLEEP MODE SUBTHRESHOLD LEAKAGE IN A BATTERY POWERED DEVICE BY MAKING LOW SUPPLY VOLTAGE LESS THAN TWICE THE THRESHOLD VOLTAGE OF ONE DEVICE TRANSISTOR	03/09/04
6,675,304	SYSTEM FOR TRANSITIONING A PROCESSOR FROM A HIGHER TO A LOWER ACTIVITY STATE BY SWITCHING IN AND OUT OF AN IMPEDANCE ON THE VOLTAGE REGULATOR	01/06/04
5,852,737	METHOD AND APPARATUS FOR OPERATING DIGITAL STATIC CMOS COMPONENTS IN A VERY LOW VOLTAGE MODE DURING POWER-DOWN	12/22/98
6,425,086	METHOD AND APPARATUS FOR DYNAMIC POWER CCONTROL OF A LOW POWER PROCESSOR	07/23/02
5,440,520	INTEGRATED CIRCUIT DEVICE THAT SELECTS ITS OWN SUPPLY VOLTAGE BY CONTROLLING A POWER SUPPLY	08/08/95
5,727,208	METHOD AND APPARATUS FOR CONFIGURATION OF A PROCESSOR OPERATING PARAMETERS	03/10/98
6,484,265	SOFTWARE CONTROL OF TRANSISTOR BODY BIAS IN CONTROLLING CHIP PARAMETERS	11/19/02
5,787,294	SYSTEM FOR REDUCING THE POWER CONSUMPTION OF A COMPUTER SYSTEM AND METHOD THEREFOR	07/28/98
5,142,684	POWER CONSERVATION IN MICROPORCESSOR CONTROLLED DEVICES	08/25/92

Foreign Patent or Published Foreign Patent Application

Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	Translation No
EP0978781	02/09/00	EPO	G06F	1/32	X	
EP0632360	01/04/95	EPO	G06F	1/32	X	

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US. Published Patent Applications

Pub. No.	Date	Applicant	Class	Sub-class	Publication Date
2002/0087896	07/04/02	Cline et al.	713	300	12/29/00

The Examiner's attention is respectfully directed to the following Documents:

"AMD ATHLON- PROCESSOR MODEL 4 DATA SHEET", No. 23792, Rev. K, November 2001, Advanced Micro Devices, Inc.

"MANUAL FOR KINETIZ 7T", 2000, QDI Computer, Inc. (USA)

"VT82C686A 'SUPER SOUTH' SOUTH BRIDGE", 02/25/00, Rev. 1.54, VIA Technologies, Inc.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060
Customer No: 000041066

Respectfully submitted,

Date: 3/22/05

By: Ronald M. Pomerenke
Reg. No. 43,009

Attorney Docket No.: TRAN-P059

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Patent Application

Inventor(s): Andrew Read, Sameer Halapete, Keith Klayman

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Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	6,704,880	03/09/04	Dai et al.	713	323	10/18/01
	B	6,675,304	01/06/04	Pole, II et al.	713	322	11/29/99
	C	5,852,737	12/22/98	Bikowsky	395	750.05	12/31/96
	D	6,425,086	07/23/02	Clark et al.	713	322	04/30/99
	E	5,440,520	08/08/95	Schutz et al.	365	226	09/16/94
	F	5,727,208	03/10/98	Brown	395	653	07/03/95
	G	6,484,265	11/19/02	Borkar et al.	713	324	12/30/98
	H	5,787,294	07/28/98	Evoy	395	750.03	10/13/95
	I	5,142,684	08/25/92	Perry et al.	395	750	06/23/89

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	Translation No
	J	EPO978781	02/09/00	EPO	G06F	1/32	X	
	K	EPO632360	01/04/95	EPO	G06F	1/32	X	

US. Published Patent Applications

Examiner Initial	No.	Pub. No.	Date	Applicant	Class	Sub-class	Publication Date
	L	2002/0087896	07/04/02	Cline et al.	713	300	12/29/00

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	M	AMD ATHLON- PROCESSOR MODEL 4 DATA SHEET", No. 23792, Rev. K, November 2001, Advanced Micro Devices, Inc.
	N	"MANUAL FOR KINETIZ 7T", 2000, QDI Computer, Inc. (USA)
	O	"VT82C686A 'SUPER SOUTH' SOUTH BRIDGE", 02/25/00, Rev. 1.54, VIA Technologies, Inc.

Examiner	Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.